



February 21, 2010

NOTICE

**M.Tech I YEAR TRIMESTER – III (2009– 2010)
FINAL EXAMINATION TIME-TABLE (MUMBAI)**

Timings 11:00 a.m. – 2:00p.m.				
Day	Day	I T	Computer	EXTC
Monday	May 10, 2010	Advanced Networking Architecture	Parallel Processing	Modern DSP
Wednesday	May 12, 2010	Multimedia System Design & Applications		Satellite Communication
Saturday	May 15, 2010	Computer Vision / Software Architecture & Project Management/ Embedded System / Fuzzy Logic & Neural Network		Telecom Network Management / Radar Engineering / Reliability & System Design /Image Processing

INSTRUCTIONS TO CANDIDATES:

1. Candidates must produce their Identity Card at the time of the examination.
2. **Candidates should know their G.R. Number which is written behind the Identity Card for appearing in the examination.**
3. Candidates will not be permitted to leave the examination hall during the first 2 hrs of the examination.
4. A Candidate will be dealt strictly if he/she is found resorting to any sort of unfair means.
5. No cell phones or any other electronic gadgets will be allowed in the examination hall that helps the student amount to unfair means.

**ASHISH APTE
CONTROLLER OF EXAMINATIONS**

**DR. D.J. SHAH
DEAN, MPSTME**