



February 15, 2010

**NOTICE**

**B. Tech II YEAR TRIMESTER – VI (2009– 2010)**  
**FINAL EXAMINATION TIME-TABLE (MUMBAI & SHIRPUR)**

Timings: 10:00 am – 1:00 pm					
Day	Date	I.T.	Computer	Electronics	Extc.
Friday	April 30, 2010	Operating System		Principles of Communication Engineering	
Monday	May 03, 2010	Computer Networks		Microprocessor Fundamentals	Signal Conditioning & Filter Design
Wednesday	May 05, 2010	Microprocessor & Microcontroller	Discrete Structures	-	Electromagnetic Field Theory
Friday	May 07, 2010	Database Management System	System Programming	Analog Integrated Circuits & Applications	Integrated Circuits & Wave Shaping Techniques

**INSTRUCTIONS TO CANDIDATES:**

1. Candidates must produce their Identity Card at the time of the examination.
2. **Candidates should know their G.R. Number which is written behind the Identity Card for appearing in the examination.**
3. Candidates will not be permitted to leave the examination hall during the first 2 hrs of the examination.
4. A Candidate will be dealt strictly if he/she is found resorting to any sort of unfair means.
5. No cell phones or any other electronic gadgets will be allowed in the examination hall that helps the student amount to unfair means.

**ASHISH APTE**  
**CONTROLLER OF EXAMINATIONS**

**DR. D.J .SHAH**  
**DEAN, MPSTME**